Lecture 12, Feb 7, 2022

Nodal Analysis

- Nodal analysis is an algorithmic method for circuit analysis; it finds the node voltages at every node in the circuit
 - Define *node voltage* as the voltage between a node and a reference point (common ground), with positive polarity at the node and negative polarity at the reference point
 - The reference (ground) node is typically denoted with a ground symbol: \downarrow or $\frac{1}{7777}$
- Apply KCL for every node in terms of the node voltages
 - Voltage between two nodes is the difference of their nodal voltages
 - $-v_{AB} = v_A v_B$ and $v_{BA} = v_B v_A$
- Procedure:
 - 1. Find all the nodes in the circuit and label them, choose one as ground
 - Choice of ground node is arbitrary but sometimes it can simplify the math
 - Choose the node that's connected to the highest number of voltage sources; prefer independent sources over dependent sources
 - 2. Assume current directions/signs (negative for current entering node, positive for current leaving)
 - 3. Write KCL for all the ungrounded notes
 - Current sources: we have voltage directly, resistors: use Ohm's law
 - If we have a voltage source between the ground node and another node, we can get the voltage of that node directly
 - Usually we always write the current that leaves a node via a resistor since it gives a positive sign
- 4. Solve the system for the nodal voltages and use the nodal voltages to find anything else neededExample circuit 1:

• Example circuit 2: